

The 1550nm cooled EML laser based 10Gigabit
SFP+

Pa N

S c a

A a

Pa a S b U M T Ma N

T a

			This pin is open collector compatible, and should be pulled up to Host Vcc with a 10kΩ resistor.
3	TX Disable	Transmitter Disable In (LVTTL)	This pin is internally pulled up to VccT with a 10 kΩ resistor.
4	SDA	Serial ID with SFF 8472 Diagnostics Module Definition Identifiers	

U₁
U₁

